



**DEPARTMENT OF ELECTRONICS ENGINEERING**  
**डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग**  
**SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT**  
**सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत**

क्रमांक: DoECE / Ph.D. Adm/ 432 / 2025-26

दिनांक: 10/06/2025

**Ph. D. Admission (Phase-II)**

**List of Eligible/ Provisionally Eligible Candidates for the DoECE, June - 2025**

**Academic Year-2025-26**

Sr. No.	App. ID	User Name	Ph.D. Cat.	Eligible / Provisionally Eligible/ Not Eligible	Sr. No.	App. ID	User Name	Ph.D. Cat.	Eligible / Provisionally Eligible/ Not Eligible
1.	11180	SVNIT3PKMD	FIR	PE	9.	11531	SVNIT3PTDJ	ERS	E
2.	11218	SVNIT3PHWQ	ERS	PE	10.	11545	SVNIT3P1P1	FIR	PE
3.	11246	SVNIT3PMLE	ERS	E	11.	11547	SVNIT3P1AY	FIR	E
4.	11254	SVNIT3PHTS	ERS	PE	12.	11573	SVNIT3PLYF	FPS	E
5.	11266	SVNIT3PK3B	ERS	E	13.	11578	SVNIT3P1BR	ERS	PE
6.	11270	SVNIT3PLO3	FIR	E	14.	11591	SVNIT3PWMM	ERS	PE
7.	11384	SVNIT3PH9K	ERS	E	15.	11662	SVNIT3PMLZ	ERS	PE
8.	11507	SVNIT3P3LT	FIR	PE					

**Note: E - Eligible, PE- Provisionally Eligible, NE- Not Eligible**

- The candidates must produce original documents with relevant mark-sheets of all semesters of B.E./B. Tech. and M.E./M. Tech. provisional passing certificate/final degree certificate at the time/day of the interview failing which they will not be allowed to participate in admission process.
- Candidates applied under OBC category must produce **Fresh NCL** in English language.
- Candidates who have applied in FIR category need to produce the GATE Score card.
- All **PE** candidates applied under **ERS(Part Time)** category for Ph.D.Admission must enclose self-attested copies of the following documents/certificates. Same must be produced in original at the time of interview/physical documents verification.
  - Appointment letter issued by the current employer.
  - Experience certificate duly signed by competent authority on the letter head of the Company/Organization/Institute.
  - Fresh Sponsorship Letter (for relieving) from his/her employer and **Fresh NOC** from his/her employer
  - All other documents like Date of Birth and Identity Proofs, all semester marksheets, degree certificates.

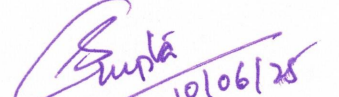
➤ For any queries contact [office@eced.svnit.ac.in](mailto:office@eced.svnit.ac.in)

**Instruction For Candidates**

- The above Candidates are requested to Report at the Department of Electronics Engineering on **26/06/2025 at 10:30 AM** with **all Original Documents**. **Short-listed candidates must remain present for the interview/presentation at Room No. EC004N Conference room, Ground Floor.**
- The Presentation/ interview will **start at 02:30 PM**. Candidates are required to give a presentation as per the following guidelines.
  - The presentation should not be more than a maximum of 10 minutes duration.
  - A PowerPoint presentation is a must.
  - The maximum number of slides including the title slide should not be more than 8.
  - The presentation should contain the background of topic, literature survey, research gap, problem identification and research methodology.
- Provisionally eligible candidates will be allowed to appear only if they submit appropriate documents at the time of document verification (with a self-attested copy).

  
 10/06/25

Dr. Z. M. Patel / Dr. J. N. Patel  
**Ph. D In-charges**

  
 10/06/25

Dr. (Ms.) Shilpi Gupta  
**Head, DoECE**